

BRUGES  
9-12 SEPTEMBER  
**ESSERC**  
2024



# PROGRAM

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**MONDAY - SEPT 9** AT BMCC

	AUDITORIUM A	AUDITORIUM B	ROOM 1+2	ROOM 3	ROOM 4+5	ROOM 6	ROOM 7+8	ROOM 10+11	ROOM 12
08:00	REGISTRATION DESK OPEN								
09:30	W14 Circuit Insights	T4	T1	W5	W9	W4	W1	T3	W7
11:00	COFFEE BREAK								
11:30	W14 Circuit Insights	T5	T6	W5	W9	W4	W1	T2	W7
13:00	LUNCH								
14:00	W14 Circuit Insights	W11	W10	W2	W8	W3	W12 Hardware Security	W6	W13
15:30	COFFEE BREAK								
16:00	W14 Circuit Insights	W11	W10	W2	W8	W3	W12 Hardware Security	W6	W13
17:30	END OF SESSIONS								

**TUESDAY - SEPT 10** AT CONCERTGEBOUW IN THE MORNING AND AT BMCC IN THE AFTERNOON

CONCERTGEBOUW								
	07:30	08:30	09:00	09:50	10:40	11:10	12:00	12:50
	REGISTRATION DESK OPEN	CONFERENCE OPENING	A0aL-H PLENARY SESSION 1 Marc Biron	A0bL-H PLENARY SESSION 2 Paul De Bot	COFFEE BREAK	A0cL-H PLENARY SESSION 3 Luca Benini	A0dL-H PLENARY SESSION 4 Christian Enz	End of plenary sessions at CONCERTGEBOUW
13:00	LUNCH at BMCC							
	AUDITORIUM A	AUDITORIUM B	ROOM 1+2	ROOM 4+5	ROOM 6	ROOM 12	ROOM 7+8	
14:00	A11-A Advanced Silicon Devices	A11-B Digital Processors for AI/ML	A11-C Amplifiers	A11-D Power Converters	A11-E High-frequency ADCs	A11-F Ultra-Low Power Wireless Techniques	A11-G Integer-N PLL based Frequency Synthesizers	
15:40	COFFEE BREAK							
16:10	A21-A Advanced Materials for Logic Devices	A21-B Digital Compute in Memory for ML	A21-C Cryo-CMOS Circuits & Devices for Quantum Computing	A21-D Wireless Rectifiers & Chargers	A21-E Optical & Transmitter Techniques for High Speed Wireline	A21-F Image Sensors	A21-G DTCO & System-Level Modeling	
17:50	END OF SESSIONS							
18:00 19:00	TPC 2025 Meeting	(SSCS Young Professionals Mentoring Event from 17:50 to 19:00 on at BMCC on LEVEL 4)						
19:30	WELCOME COCKTAIL at the Brewery De Halve Maan (Walplein, 26 - 8000 Brugge)							

**WEDNESDAY - SEPT 11** AT BMCC

	AUDITORIUM A	AUDITORIUM B	ROOM 1+2	ROOM 4+5	ROOM 6	ROOM 12	ROOM 7+8
08:00	REGISTRATION DESK OPEN						
08:45	B0L-A PLENARY SESSION 5 Aida Todri-Sanial						
09:45	COFFEE BREAK						
10:30	B11-A Power & mmWave Devices	B11-B Systems on Chip & Accelerators	B11-C References & Filters	B11-D 5G-6G Frontends	B11-E Wireline Receiver & Signalling Techniques	B11-F Low Phase Noise LC-VCOs	B11-G Devices for Imaging
12:10	LUNCH						
13:30	B21-A Reliability & Characterization	B21-B SRAM-based Analog Compute-in-Memory	B21-C Innovations in SAR ADCs	B21-D Above One Decade BW Building Blocks	B21-E Emerging Devices	B21-F Biomedical Sensors	B21-G Cellular & IoT Subsystems
15:10	COFFEE BREAK						
15:40	B31-A SOI Based Devices	B31-B Cryptographic Accelerators	B31-C Cryo-CMOS Circuits for Trapped-Ion Quantum Computer (3 papers)	B31-D mmWave Frontend Solutions	B31-E Analog Techniques	B31-F Resonators & Magnetic Sensors	B31-G Energy Harvesters
17:20	END OF SESSIONS						
18:30	GALA DINNER at Lauretum Buses will leave from Magdalenastraat 30 in Bruges at 18:30. The Gala Dinner will end at 22:30. Buses will return to Magdalenastraat 30 in Bruges.						

**THURSDAY - SEPT 12** AT BMCC

	AUDITORIUM A	AUDITORIUM B	ROOM 1+2	ROOM 4+5	ROOM 6	ROOM 12	ROOM 7+8
08:00	REGISTRATION DESK OPEN						
08:30	COL-A PLENARY SESSION 6 Gabriele Manganaro						
09:30	ESSERC 2025 Announcement						
09:45	ESSCIRC ESSDERC 2023 Paper Awards						
10:00	50 Years of ESSERC!						
10:15	COFFEE BREAK						
11:00	C11-A Modeling of Emerging Devices	C11-B Advances in Analog Compute-in-Memory Architectures & Memory Technologies	C11-C DC/DC Converters	C11-D Digital Circuit Design Techniques	C11-E Wireline Clock Generation & Recovery	C11-F Novelties in Noise-shaping ADCs	
12:40	LUNCH						
13:40	C21-A Advanced Memory Devices (5 papers)	C21-B Automotive & Resilient Circuits	C21-C Low-jitter Fractional-N PLLs & Timing Generation	C21-D D-band & Beyond Front End Circuits (3 papers)	C21-E Emerging Computing Systems	C21-F Compact Modeling (5 papers)	
15:20	END OF SESSIONS						